

## INITIAL INFORMATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 03-29-2004  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: A Programmable Logic Device with  
Time-Multiplexed Interconnect  
Docket Number:: X-1006-2C US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 08  
Small Entity?:: No  
Petition included?:: No

### INVENTOR INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Stephen  
Middle Name:: M.  
Family Name:: Trimberger  
Street:: 1261 Chateau Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95120

### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

### REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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**DOMESTIC PRIORITY INFORMATION**

<b>Application::</b>	<b>Continuity Type::</b>	<b>Parent Application::</b>	<b>Parent Filing Date::</b>
This Applic.	Continuation	10/253,740	09-23-02

**SSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124